

PTO/SB/30 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031
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REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995.
See The American Inventors Protection Act of 1999 (AIPA).

Application Number	09/832,685
Filing Date	April 10, 2001
Examiner Name	Daniel D. Chang
First Named Inventor	Reddy, Srinivas
Group Art Unit	2819
Attorney Docket Number	015114-045720US

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.
NOTE: 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53 (d) (PTO/SB/29) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and Provisional Application Practice, Interim Rule, 65 Fed. Reg. 14865 (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

1. **Submission required under 37 C.F.R. § 1.114**

- a. ☐ Previously submitted
- i. ☐ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on _____
(Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on _____
- iii. ☐ Other _____
- b. ☐ Enclosed
- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☒ Other Certificate of Transmittal

2. **Miscellaneous**

- a. ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a period of _____ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17(i) required)
- b. ☐ Other _____

3. **Fees** The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.

- a. ☒ The Director is hereby authorized to charge any fees which may be required, or credit any overpayments, to Deposit Account No. 20-1430. A duplicate of this sheet is enclosed.
- i. ☒ RCE fee required under 37 C.F.R. § 1.17(e)
- ii. ☐ Extension of time fee (37 C.F.R. §§ 1.136 and 1.17)
- iii. ☐ Other _____
- b. ☐ Check in the amount of \$ _____ enclosed
- c. ☐ Payment by credit card (Form PTO-2038 enclosed)

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

Name (Print /Type)	Steven Cahill	Registration No. (Attorney/Agent)	44,578
Signature	<i>Steven Cahill</i>	Date	April 3, 2003

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office, Fax No. (703) 872-9318 on April 3, 2003

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PTO/SB/97 (08-00)

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DJ Bogorodsky

Typed or printed name of person signing Certificate

In re: application of: Srinivas Reddy et al.
Application Number: 09/832,685
Filed: April 10, 2001
Title: Tristate Structures for Programmable Logic Devices
Atty Docket Number: 015114-045720US SJC/djb

Being faxed to Examiner - Daniel D. Chang Group 2819 at facsimile number
1-703-872-9318 are the following documents:

This PTO/SB/97 Certificate of Transmission (1 page);
PTO/SB/30 RCE transmittal (1 page submitted in duplicate);
Amendment (10 pages)

Number of pages being transmitted: 13

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Attorney Docket No.: 015114-045720US
Client Ref. No.: A21151 C1

TOWNSEND and TOWNSEND and CREW LLP

By: DJ Bogorodsky

DJ Bogorodsky

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Srinivas Reddy et al.

Application No.: 09/832,685

Filed: April 10, 2001

For: TRISTATE STRUCTURES FOR
PROGRAMMABLE LOGIC DEVICES

Examiner: Daniel D. Chang

Art Unit: 2819

AMENDMENT

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TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action mailed October 1, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claim 59 and amend claims 33, 48, 52, 56, and 58 as follows:

Sub D7
C1

33. (currently amended) A method of multiplexing signals onto an interconnect line comprising
enabling a first tristate driver having first and second driver transistors, an input coupled to a first logic element and an output directly connected to the interconnect line, such that a first signal is driven from the first logic element onto the interconnect line using the first tristate driver;
dynamically tristating the first tristate driver; and